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PRIMARY PATENT EXAMINER

PATENT

IBM/276  
Confirmation No. 9305

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants:	Toshiharu Furukawa, et al.	Confirmation No. 9305
Serial No.:	10/732,958	
Filed:	December 11, 2003	
Group Art Unit:	2891	
Examiner:	Douglas M. Menz	
Title:	<b>WRAP-AROUND GATE FIELD EFFECT TRANSISTOR</b>	
Atty. Docket No.:	ROC920030282US1	

**AMENDMENT UNDER 37 C.F.R. 312**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Notice of Allowance of April 4, 2007, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Remarks** begin on page 4 of this paper.

An **Appendix** including a clean version of the amended Abstract of the Disclosure is attached following page 4 of this paper.